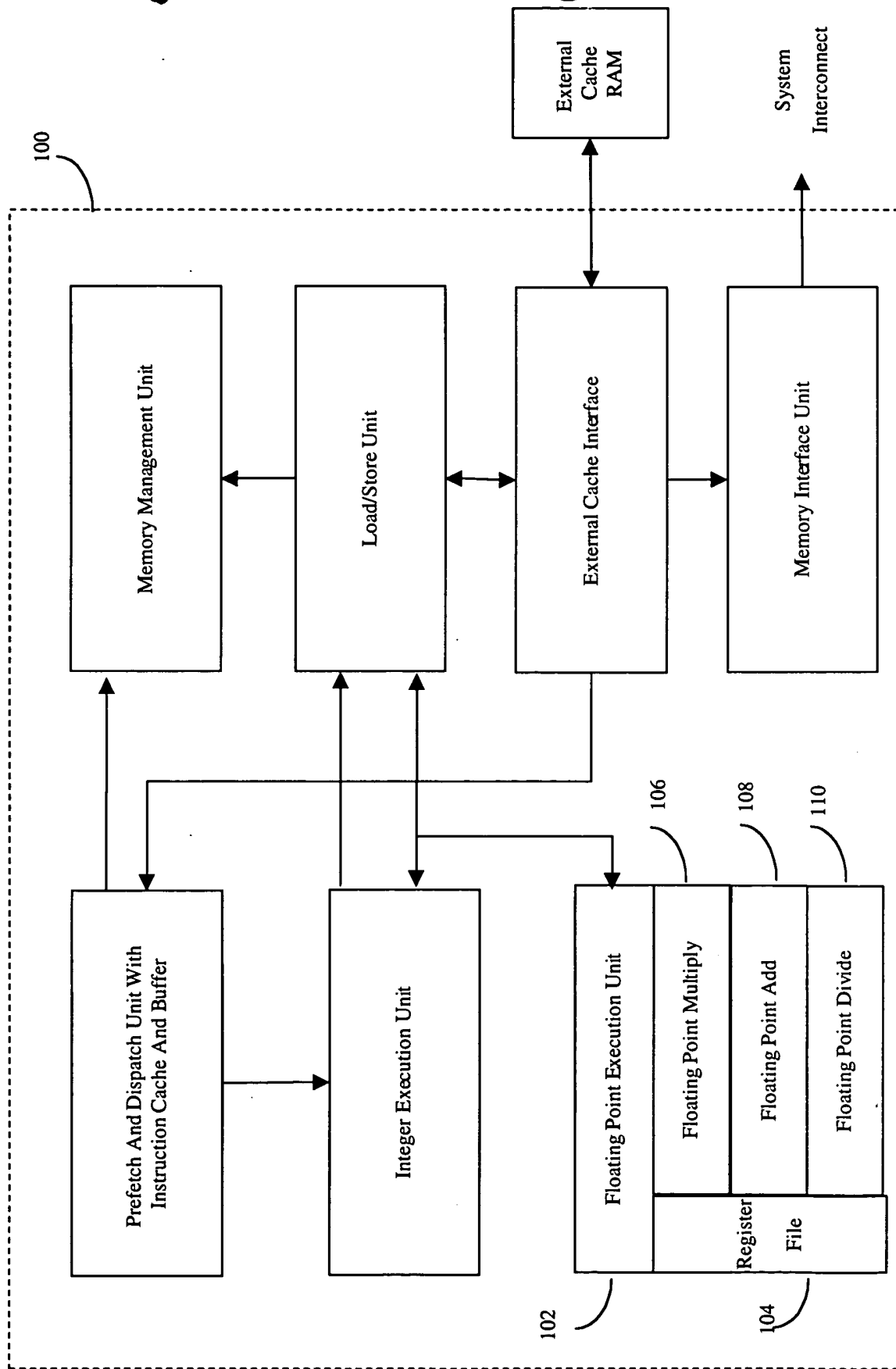
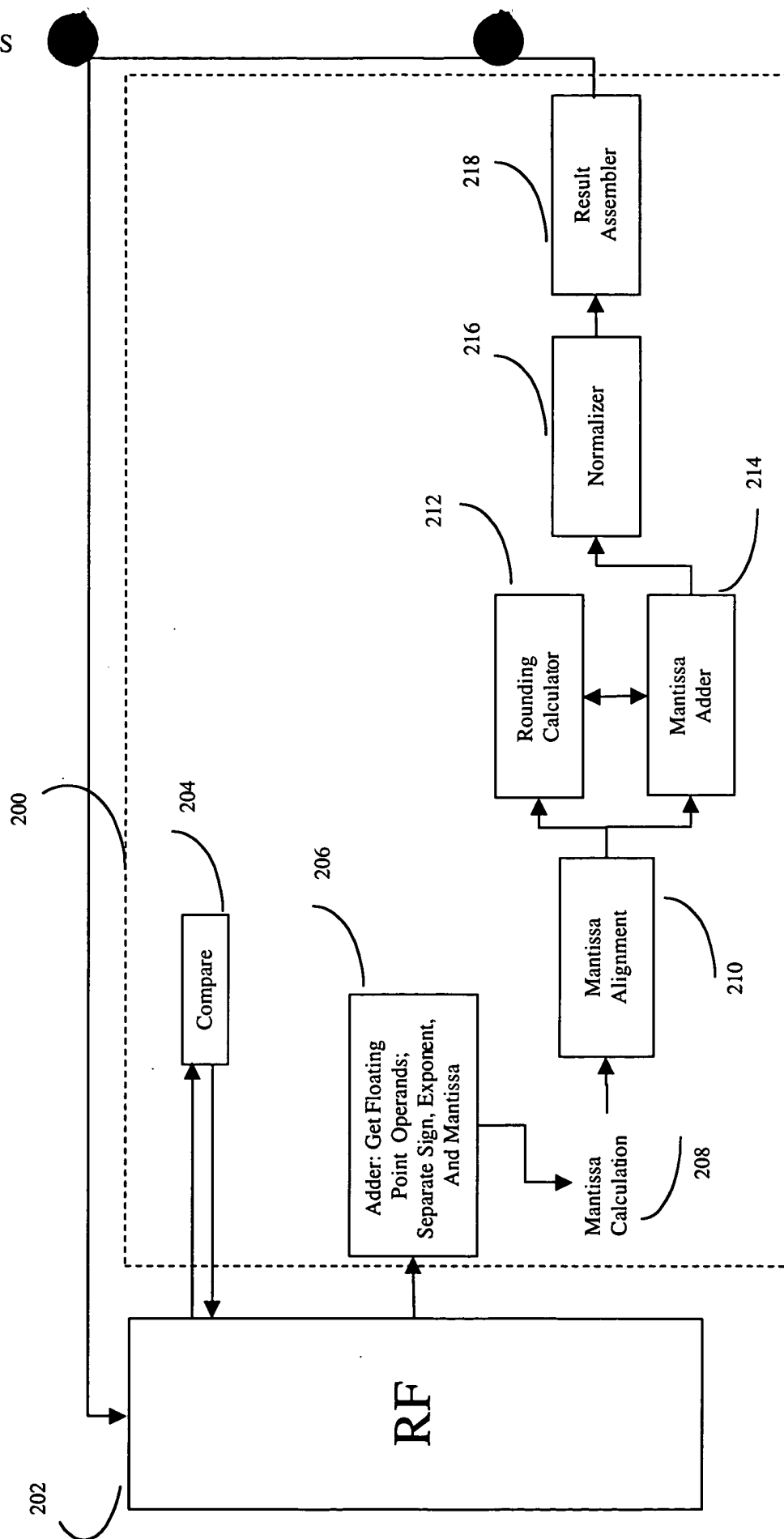


FIGURE 1

Typical Prior Art Processor Architecture



**Floating Point Execution Unit:
Block Diagram Of Relevant Functionality
(Prior Art)**



302



Generate Sum and Sum+1

Calculate Rounding Using GRS (And Other Available Input)

Generate Output

Select Sum or Sum+1

Figure 4

Block Diagram Of Relevant Portion Of A Floating Point Execution Unit With Present Invention

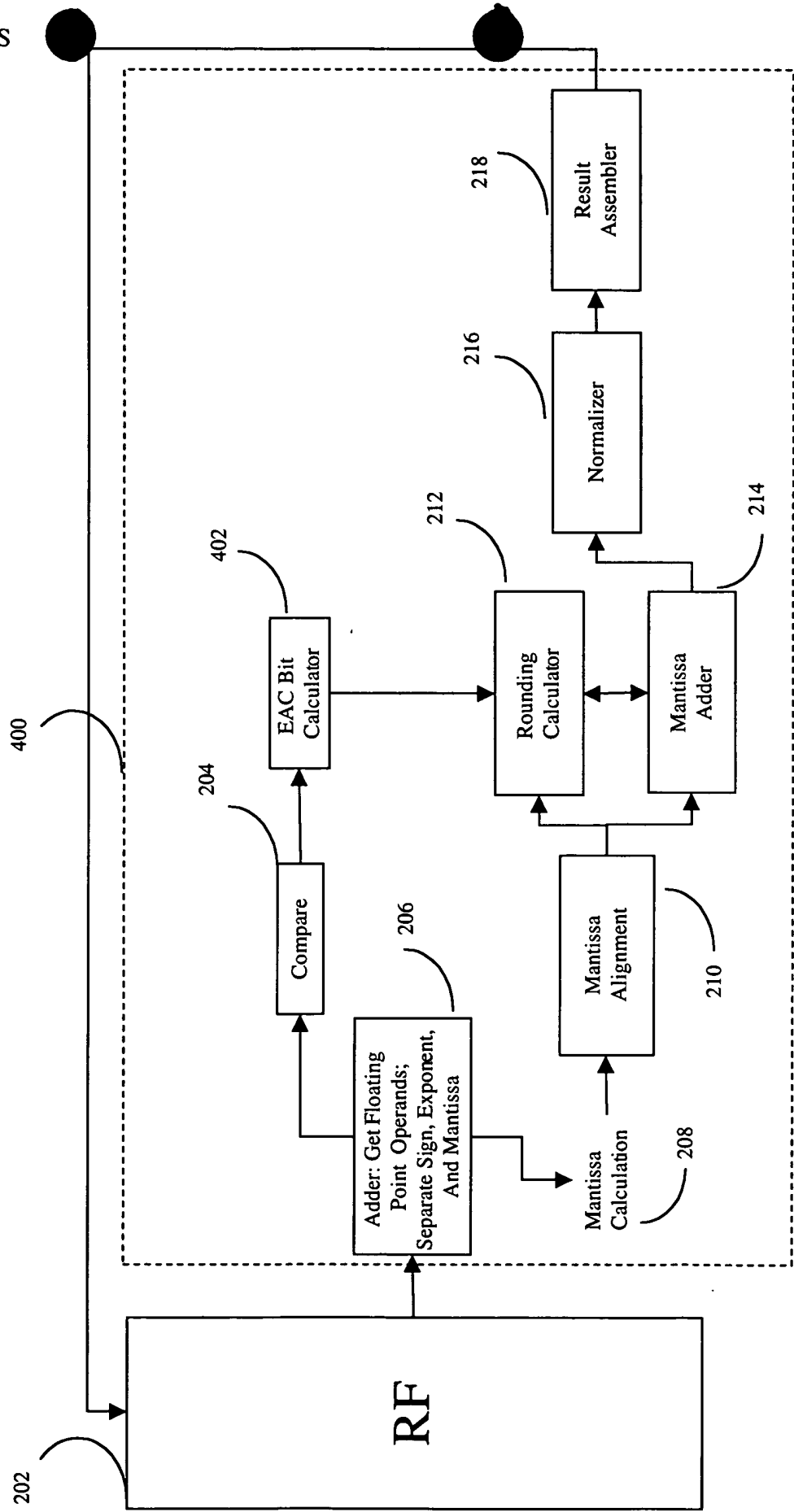


Figure 5
Functional Pipeline
With Present Invention

